## JOHN WHITE

*Corporate Vice President*
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John White is corporate vice president of engineering and product technology development for the Display and Flexible Technology Business Group at Applied Materials, Inc. He leads development of Applied's Display products. He was named an Applied Materials Fellow in 2010 for contributions to the company's semiconductor, display and solar product lines that have helped bring world-class technology and products to Applied customers.

Starting in 2005, Mr. White drove development of Gen-6 through Gen-10 chemical vapor deposition (CVD), Gen-8.5 PiVot DT physical vapor deposition (PVD), low temperature polysilicon (LTPS) CVD and original development of metal-oxide semiconductor products. These products and applications have grown the company's total available market significantly and earned wide customer acceptance to make Applied a global leader in Display manufacturing technology. In 2013, the Display team's achievements in product development were recognized by the prestigious IEEE Corporate Innovation Award. Since 2017, Mr. White has moved to finding new Display technology and product growth opportunities.

Mr. White first joined Applied in July 1983 as a mechanical engineer in the semiconductor CVD division and went on to lead a team that developed the highly successful Precision 5000 system. Between 1998 and 2000, he served as deputy general manager of the chemical mechanical planarization division.

Previously, Mr. White was an engineer at Lawrence Livermore National Laboratory from 1979 to 1983.

Mr. White received a bachelor of science degree in mechanical engineering from the University of Arizona, Tucson. He holds more than 455 patents in the field of semiconductor and flat panel display processing equipment.